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## PATENT APPLICATION

ATTORNEY DOCKET NO. 200313645-1

Inventor(s): Theodore C Briggs et al

Confirmation No.: 2130

Application No.: 10/756448

Examiner: Unknown

Filing Date: Jan 12, 2004

Group Art Unit:

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Title: Memory Controller Connection To RAM Using Buffer Interface

Commissioner For Patents  
PO Box 1450  
Alexandria, VA 22313-1450

STATUS INQUIRYMore than 24 months have passed since:

- ☒ NEW APPLICATIONS  
The filing of this application on Jan 12, 2004. No communication has been received from the Patent and Trademark Office indicating action on this application.
- ☐ AMENDED APPLICATIONS  
The filing of a response on \_\_\_\_\_. No communication has been received from the Patent and Trademark Office.
- ☐ APPEALED APPLICATION  
The Appeal Brief was filed on \_\_\_\_\_.  
☐ An Examiner's Answer was mailed on \_\_\_\_\_.  
☐ A Reply Brief to the Examiner's Answer was submitted on \_\_\_\_\_.
- ☐ ALLOWED APPLICATIONS  
The mailing of FORM POL-85 and/or Examiner's Amendment on \_\_\_\_\_.

Kindly advise the undersigned of the present status of this application.

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Date of facsimile: 2/27/06  
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Respectfully submitted,

By 

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Date: 2-23-06

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